405

Addr	Instruction	Bytes	Cycles	
53	LJMP (IRQ0JT+offset)	3	4	(IRQ4JT+offset) is set by the IRQ state machine based on the active sub-IRQ4 interrupt
54	-			
55	-			
56	-			
57	-			
58	-			
59	-			
5A	-	-	-	

410

FIG. 4A

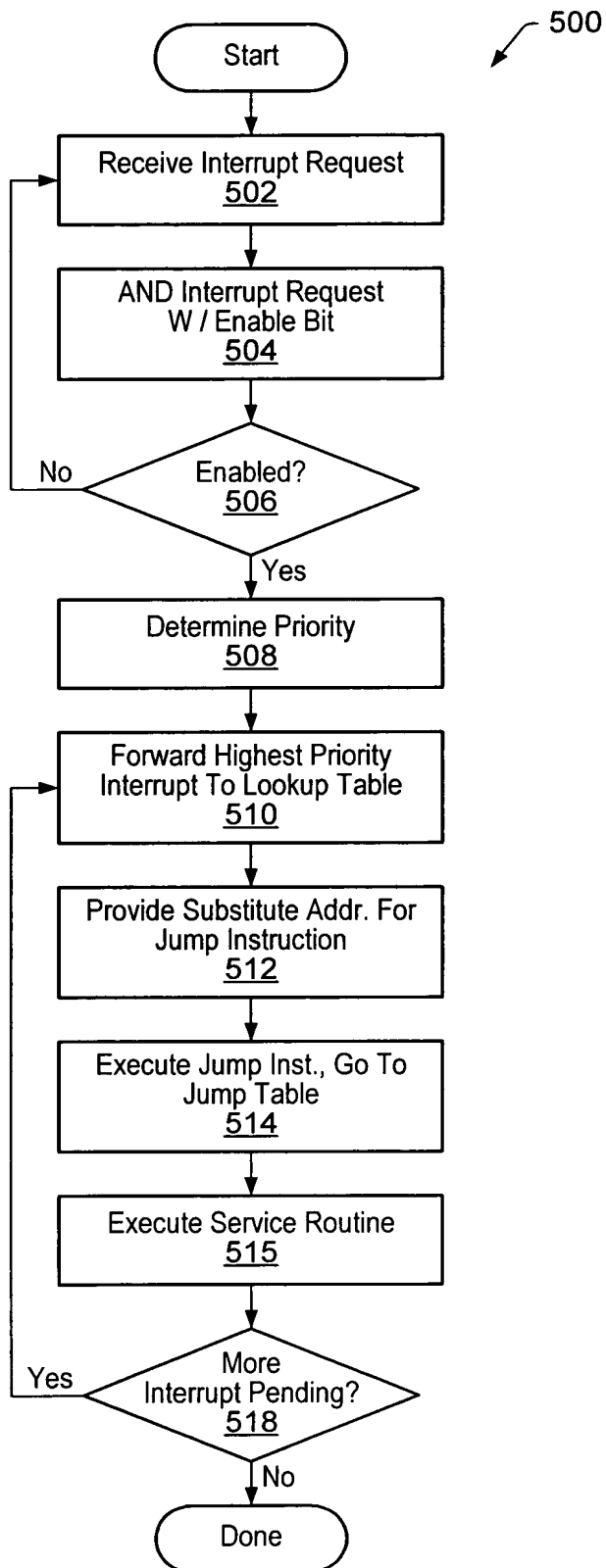| Addr	Instruction	Bytes	Cycles	Comment
IRQ4JT				IRQ4 Jump Table The location of this table is fixed by hardware and cannot be moved.
+00	LJMP IRQ4.0	3	4	IRQ4 Bit 0 service routine
+04	LJMP IRQ4.1	3	4	IRQ4 Bit 1 service routine
+08	LJMP IRQ4.2	3	4	IRQ4 Bit 2 service routine
+0C	LJMP IRQ4.3	3	4	IRQ4 Bit 3 service routine
+10	LJMP IRQ4.4	3	4	IRQ4 Bit 4 service routine
+14	LJMP IRQ4.5	3	4	IRQ4 Bit 5 service routine
+18	LJMP IRQ4.6	3	4	IRQ4 Bit 6 service routine
+1C	LJMP IRQ4.7	3	4	IRQ4 Bit 7 service routine
+20	LJMP IRQ none	3	4	No IRQ4's are active-go to a null routine. This could have just a RETI or other processing.

6 / 6

FIG. 5